

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
10 February 2005 (10.02.2005)

PCT

(10) International Publication Number
WO 2005/013562 A1

(51) International Patent Classification⁷: **H04L 12/56**

(21) International Application Number:
PCT/AU2004/001037

(22) International Filing Date: 28 July 2004 (28.07.2004)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
2003903958 29 July 2003 (29.07.2003) AU

(71) Applicant (for all designated States except US): **CORTEC SYSTEMS PTY LTD** [AU/AU]; PO Box 1327, West Perth, W.A. 6872 (AU).

(72) Inventors; and

(75) Inventors/Applicants (for US only): **SILQUINI, John** [AU/AU]; 12 Elstree Avenue, Menora, W.A. 6050 (AU). **CANTONI, Antonio** [AU/AU]; 10 Yallambee Place, City Beach, W.A. 6015 (AU). **IVANDICH, Steven** [AU/AU]; 84 Solomon Street, Fremantle, W.A. 6160 (AU). **MERCANKOSK, Guven** [AU/AU]; 88A Hobbs Avenue,

Como, W.A. 6152 (AU). **GIBSON, Kent** [AU/AU]; 21 McPherson Avenue, Noranda, W.A. 6062 (AU). **WARD, David** [AU/AU]; 54 Adair Parade, Menora, W.A. 6050 (AU).

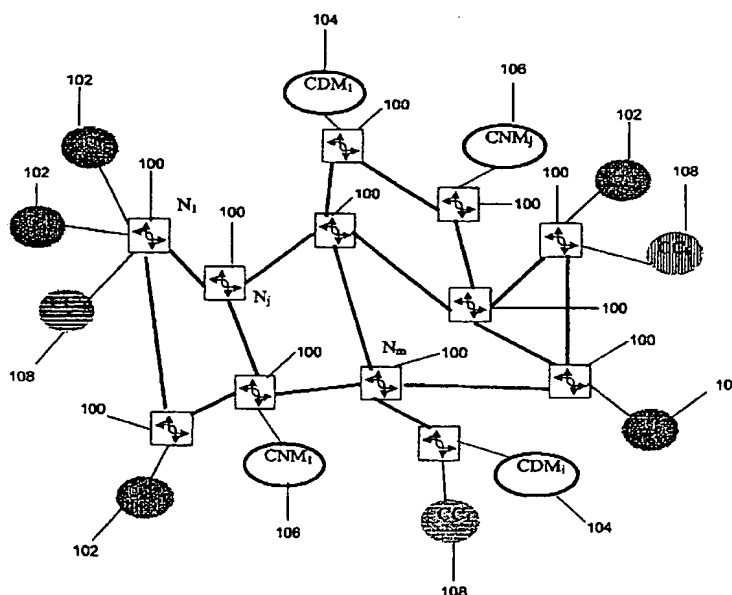
(74) Agent: **WRAY & ASSOCIATES**; Level 4, The Quadrant, 1 William Street, Perth, W.A. 6000 (AU).

(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PL, PT, RO, SE, SI,

[Continued on next page]

(54) Title: VIRTUAL CIRCUITS IN PACKET NETWORKS



(57) Abstract: Systems and methods for provisioning a virtual circuit having a predetermined Quality of Service (QoS) in packet networks such as the Internet. A system for provisioning a virtual circuit having a predetermined QoS is provided for a packet network, consisting of (1) a circuit client (108) connected to the network and able to generate a request for a packet flow between two nodes at a predetermined QoS; and (2) a circuit manager (CM) able to receive the circuit client's (108) request and process such request in order to provide a route between the two nodes, such route having the predetermined QoS.



SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

Published:

— *with international search report*